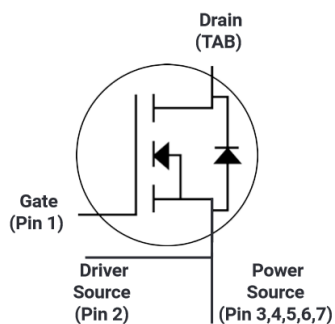


Silicon Carbide Power MOSFET (N-Channel Enhancement)

V_{DS}	1200V
I_D (25°C)	52A
$R_{DS(on)}$	34mΩ



Features

- High speed switching
- Essentially no switching losses
- Reduction of heat sink requirements
- Maximum working temperature at 175 °C
- High blocking voltage
- Fast Intrinsic diode with low recovery current
- High-frequency operation
- Halogen free, RoHS compliant

Typical Applications

Typical applications are in power factor correction(PFC), solar inverter, uninterruptible power supply, motor drives, photovoltaic inverter, electric car and charger.

Mechanical Data

- **Package:** TO-263-7L
- **Terminals:** Tin plated leads
- **Polarity:** As marked

■Maximum Ratings (T_c=25°C Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	VALUE	TEST CONDITIONS	NOTE
Device marking code			D212040B7YG3		
Drain source voltage @ T _j =25°C	$V_{DS,max}$	V	1200	$V_{GS}=0V$, $I_D=100\mu A$	
Gate source voltage @ T _j =25°C	$V_{GS,max}$	V	-8/+22	Absolute maximum values (AC f > 1Hz, duty cycle < 1%)	
Gate source voltage @ T _j =25°C	$V_{GS,op}$	V	-5/+18	Recommended operational values	
Continuous drain current @ T _c =25°C	I_D	A	52	$V_{GS}=18V$, T _c =25°C	Fig.17
Continuous drain current @ T _c =100°C			36	$V_{GS}=18V$, T _c =100°C	
Pulsed drain current	$I_{D(pulsed)}$	A	180	Pulse width t _p limited by T _{j,max}	Fig.22
Avalanche energy, Single pulse	E_{AS}	mJ	870	$V_{DD}=75V$, L=30mH	
Power Dissipation	P_{TOT}	W	192	T _c =25°C, T _j = 175°C	Fig.16
Power Dissipation			96	T _c =100°C, T _j = 175°C	
Operating junction and Storage temperature range	T _j , T _{stg}	°C	-55 to +175		
Soldering temperature	T _L	°C	260	1.6mm (0.063") from case for 10s	
Mounting torque	T _M	Nm	0.6	M3 screw Maximum of mounting process: 3	



■Static Electrical Characteristics (Tc=25℃ unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Gate threshold voltage	$V_{GS(th)}$	V	2.0	3.0	4.0	$V_{DS}=V_{GS}$, $I_D=12mA$	Fig.4, 11
				2.2		$V_{DS}=V_{GS}$, $I_D=12mA$, $T_j=175^\circ C$	
Drain source breakdown voltage	$V_{(BR)DSS}$	V	1200			$V_{GS}=0V$, $I_D=100\mu A$	
Drain source leakage current	I_{DSS}	μA		10	100	$V_{DS}=1200V$, $V_{GS}=0V$	
Gate source leakage current	I_{GSS}	nA		10	100	$V_{GS}=18V$, $V_{DS}=0V$	
Current drain source on-state resistance	$R_{DS(on)}$	m Ω		40		$V_{GS}=15V$, $I_D=30A$	Fig.5, 6, 7
				34	42	$V_{GS}=18V$, $I_D=30A$	
				62		$V_{GS}=15V$, $I_D=30A$, $T_j=175^\circ C$	
				56		$V_{GS}=18V$, $I_D=30A$, $T_j=175^\circ C$	
Transconductance	g_f	S		19		$V_{DS}=20V$, $I_D=30A$	Fig.4
				18		$V_{DS}=20V$, $I_D=30A$, $T_j=175^\circ C$	

■Dynamic Electrical Characteristics (Tc=25℃ unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Input capacitance	C_{iss}	pF		3362		$V_{DS}=1000V$, $V_{GS}=0V$, $T_j=25^\circ C$, $f=1MHz$, $V_{AC}=25mV$	Fig.13, 14
Output capacitance	C_{oss}			119			
Reverse capacitance	C_{rss}			5.4			
Coss stored energy	E_{oss}	μJ		85			Fig.15
Gate source charge	Q_{gs}	nC		31		$V_{DS}=1000V$, $V_{GS}=-5/18V$, $I_D=30A$	Fig.12
Gate drain charge	Q_{gd}			58			
Gate charge	Q_g			117			
Internal gate resistance	R_g	Ω		1.6		$f=1MHz$	

■Switching Characteristics (Tc=25℃ unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Turn on delay time	$t_{d(on)}$	ns		12.5		$V_{DD}=1000V$, $V_{GS}=-5/+18V$, $I_D=30A$, $R_g=2.4\Omega$, $L=100\mu H$	Fig.21
Rise time	t_r			18			
Turn off delay time	$t_{d(off)}$	ns		29			
Fall time	t_f			10			



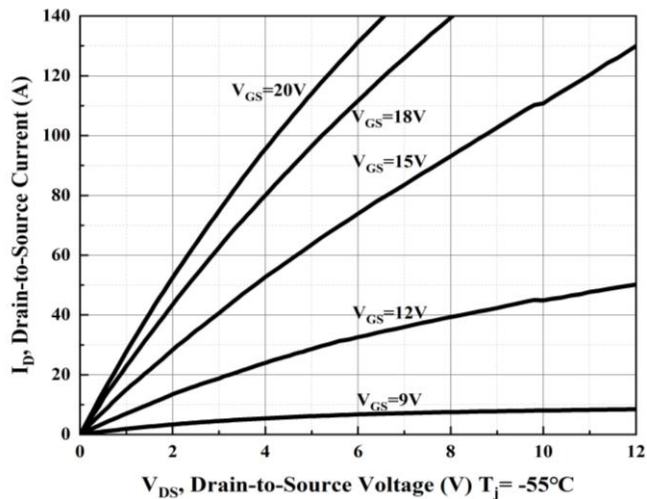
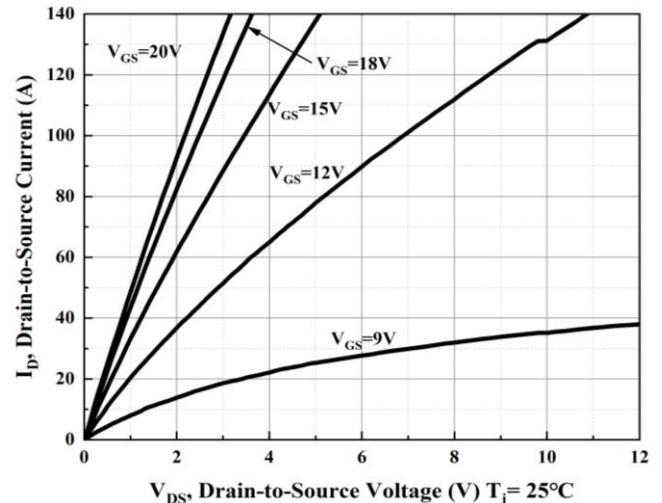
Turn on switching energy	E_{on}	μJ		320		$V_{DD}=1000V, V_{GS}=-5/+18V, I_D=30A,$ $R_g=2.4\Omega, L=100\mu H$	Fig.19, 20
Turn off switching energy	E_{off}			98			

■Body diode characteristics ($T_c=25^\circ C$ unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Diode forward voltage	V_{SD}	V		4.7		$V_{GS}=-5V, I_{SD}=15A$	Fig.8
				3		$V_{GS}=0V, I_{SD}=15A, T_j=175^\circ C$	Fig.9
Continuous diode forward current	I_S	A		34		$V_{GS}=-5V, T_c=25^\circ C$	
Reverse recovery time	t_{rr}	nS		17		$V_R=800V, V_{GS}=-5V, I_D=30A,$ $di/dt=2000A/\mu S$	
Reverse recovery charge	Q_{rr}	nC		178			
Peak reverse recovery current	I_{rrm}	A		16			

■Thermal Characteristics ($T_a=25^\circ C$ Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Typ.
Thermal resistance	$R_{\theta J-C}$	$^\circ C/W$	0.78

■Typical CharacteristicsFigure 1. Output Characteristics $T_j = -55^\circ C$ Figure2. Output Characteristics $T_j = 25^\circ C$

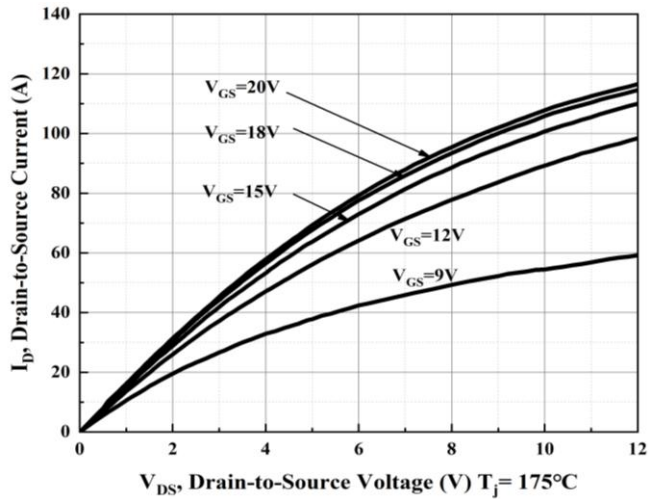


Figure 3. Output Characteristics $T_j = 175^\circ\text{C}$

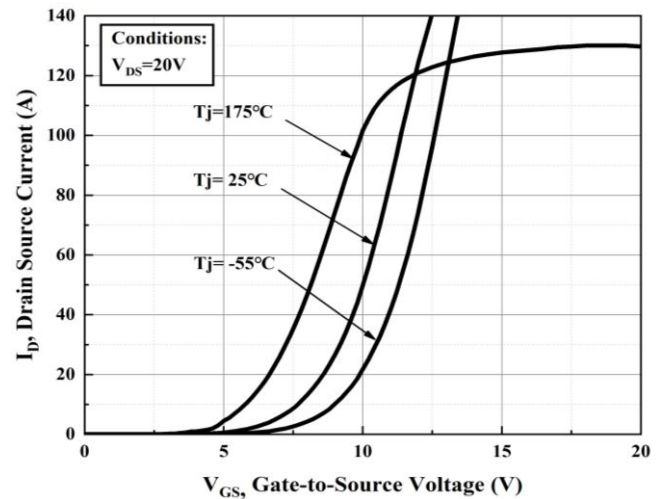


Figure 4. Transfer Characteristics for Various Junction Temperature

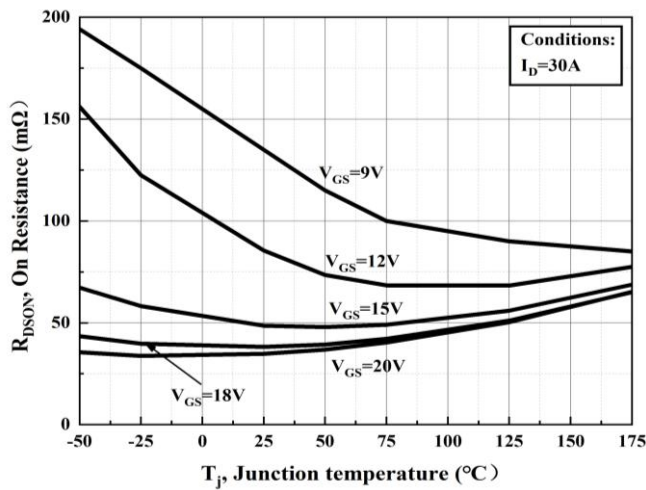


Figure 5. On-resistance vs. Temperature for Various Gate Voltage

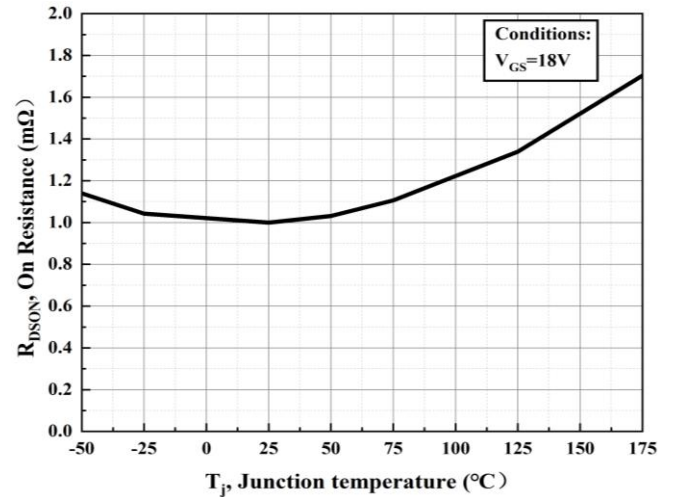


Figure 6. Normalized on-resistance vs. Temperature

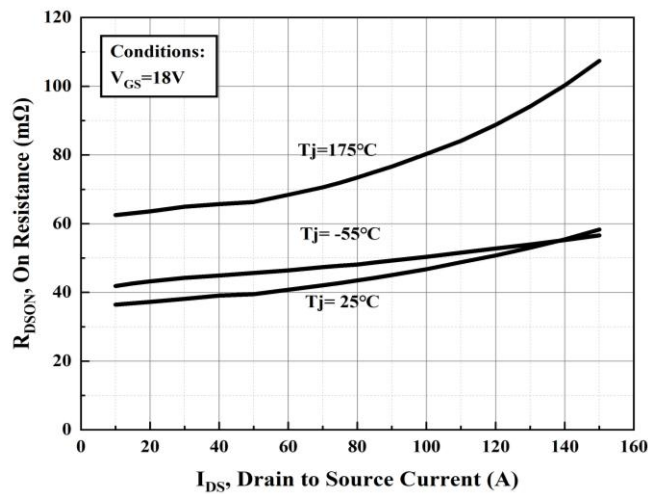


Figure 7. On-resistance vs. Drain Current

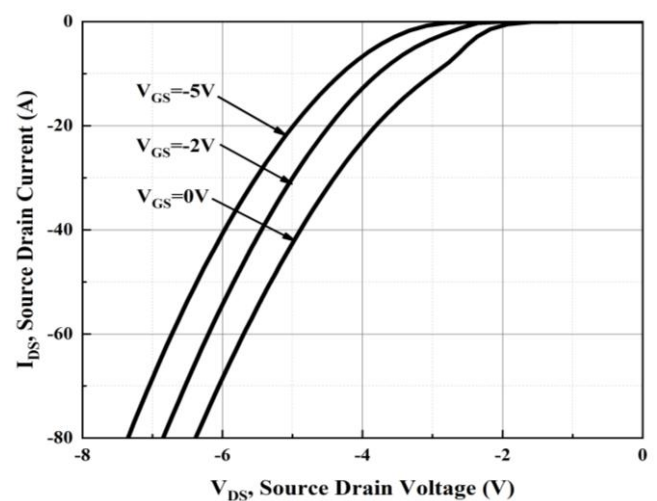


Figure 8. Body Diode Characteristic at $T_j = 25^\circ\text{C}$

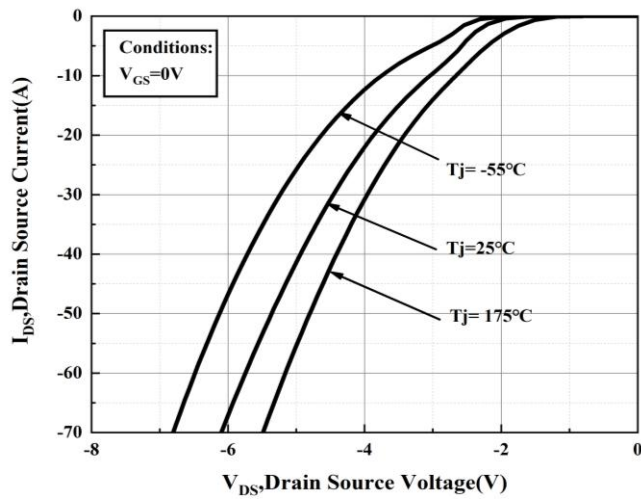


Figure 9. Body Diode Characteristic

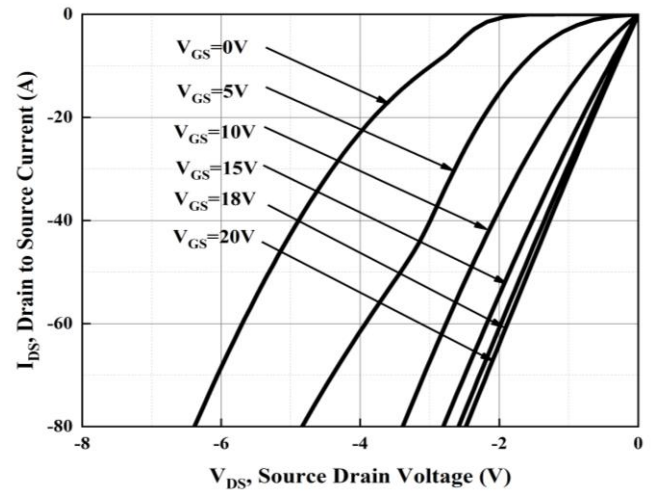


Figure 10. 3rd quadrant Characteristic at Tj = 25 °C

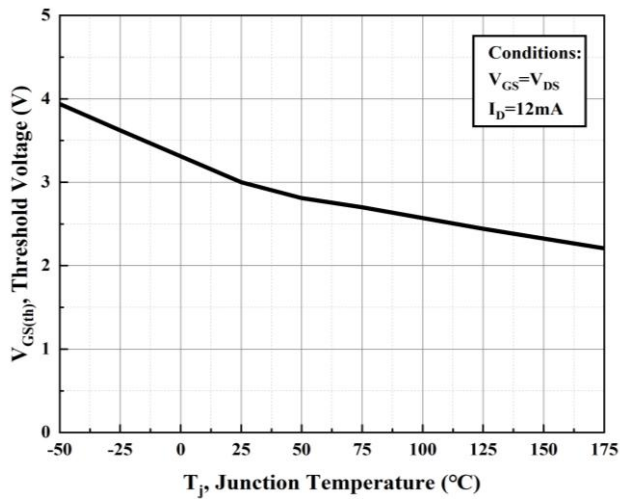


Figure 11. Threshold Voltage vs. Temperature

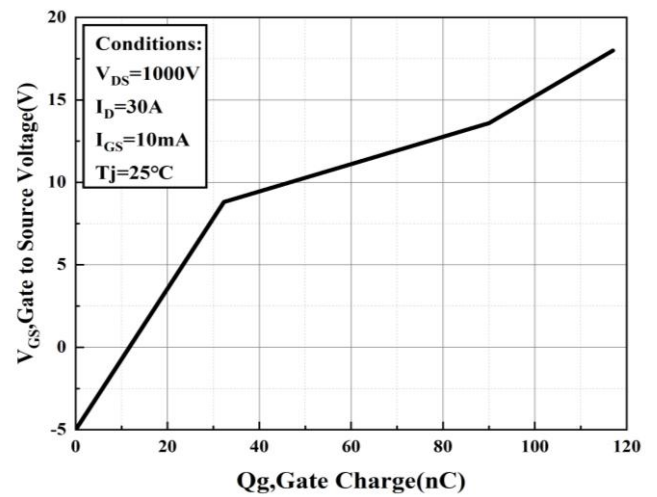


Figure 12. Gate Charge Characteristic

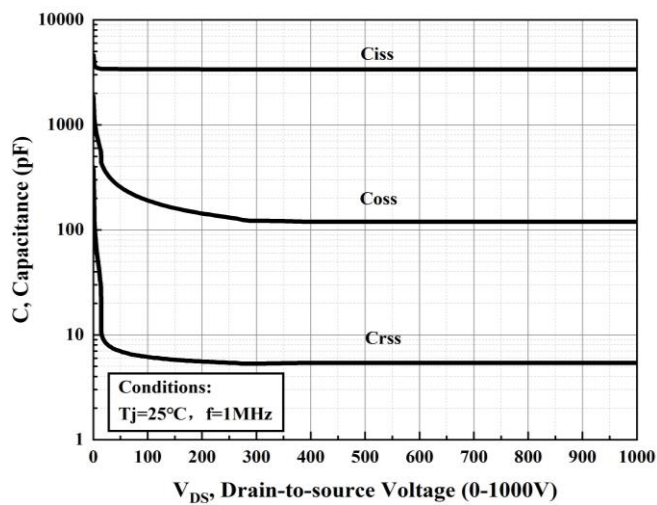


Figure 13. Capacitances vs. Drain Source Voltage (0-1000V)

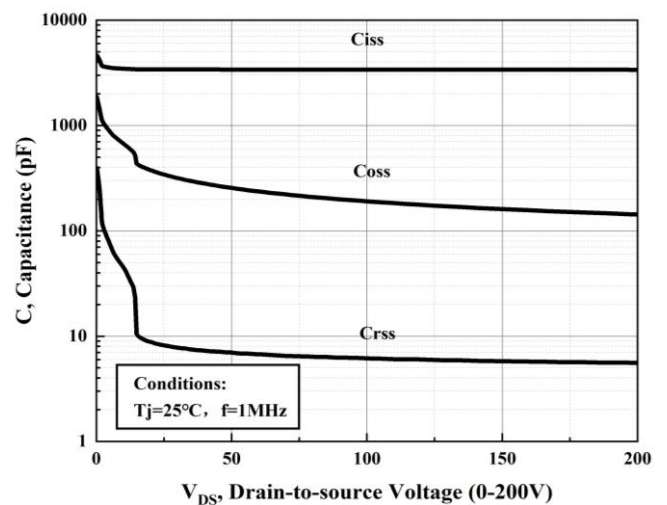


Figure 14. Capacitances vs. Drain Source Voltage (0-200V)

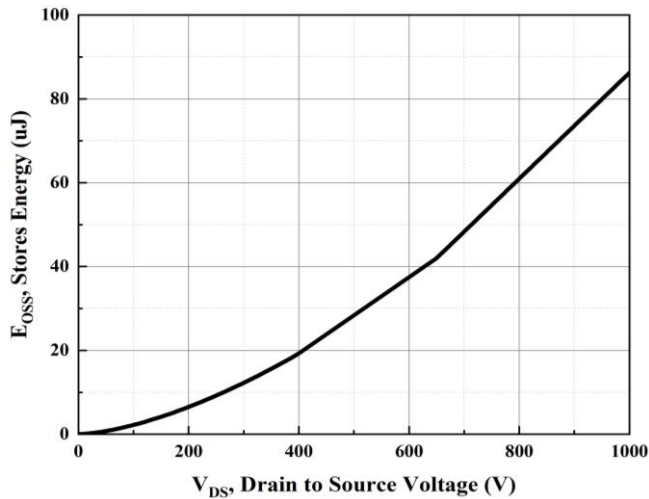


Figure 15. Output Capacitor Stored Energy

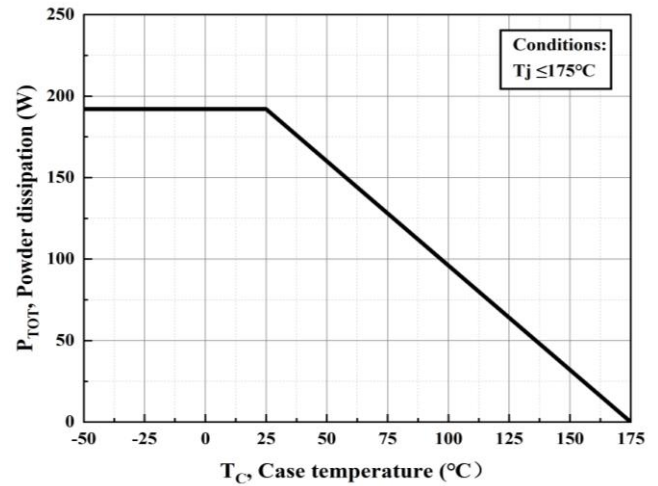


Figure 16. Maximum Power Dissipation Derating vs. Case Temperature

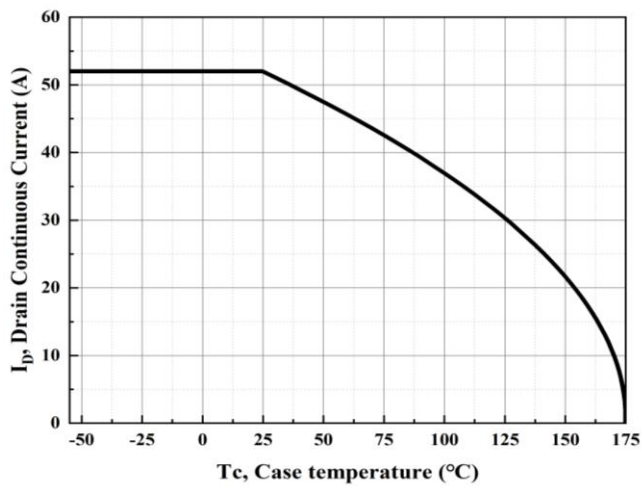


Figure 17. Continuous Drain Current Derating vs. Case Temperature

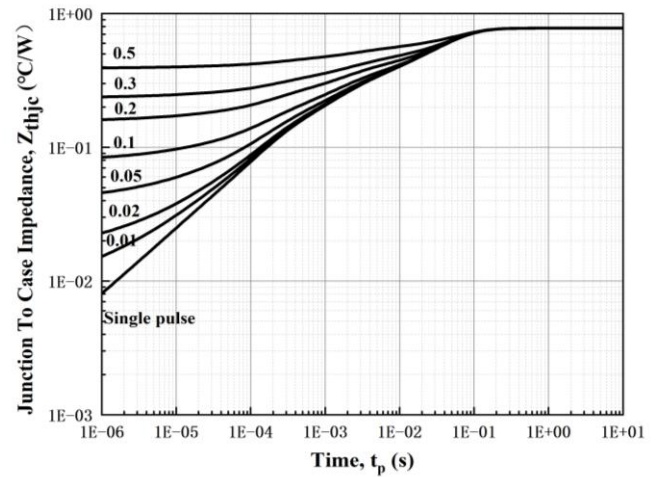


Figure 18. Transient Thermal Impedance (Junction - Case)

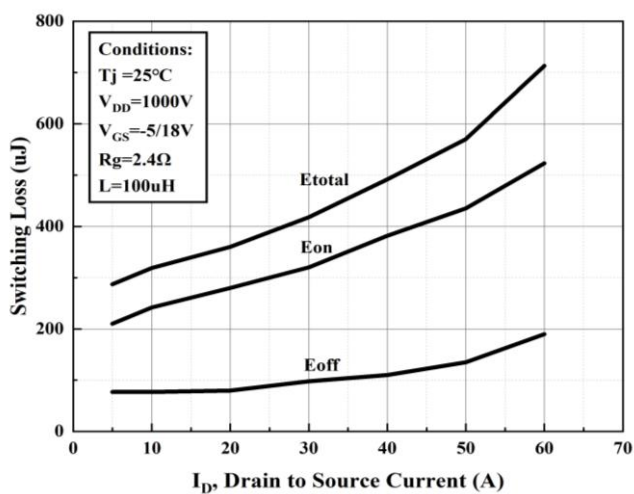


Figure 19. Clamped Inductive Switching Energy vs. Drain Current

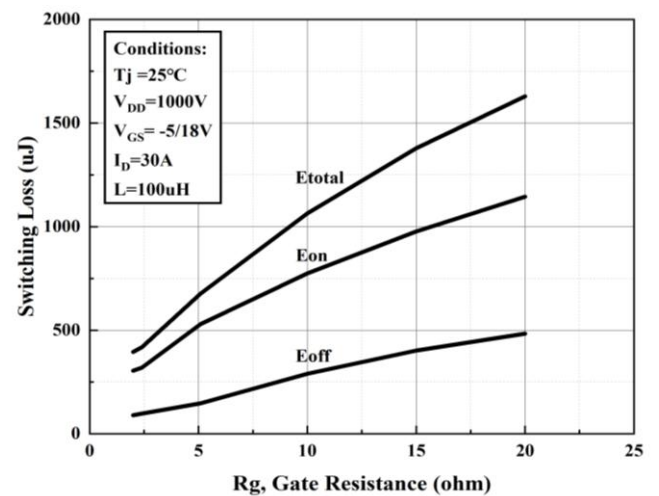


Figure 20. Clamped Inductive Switching Energy vs. R_g

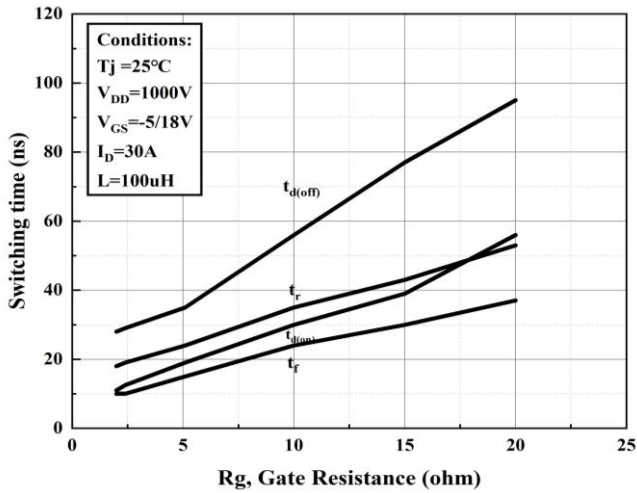


Figure 21. Switching Times vs. R_g

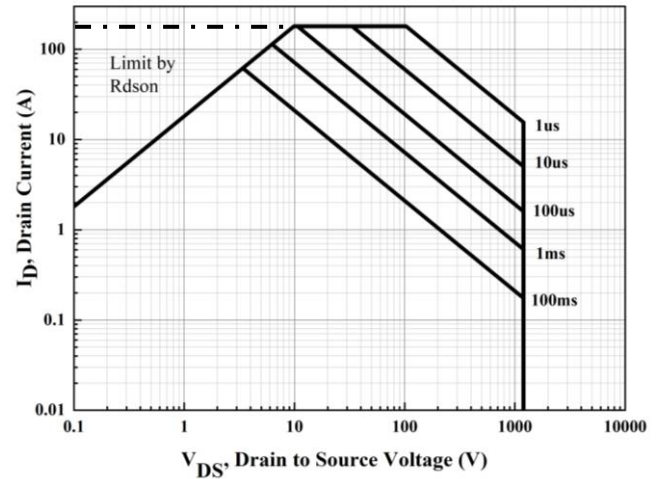


Figure 22. Safe Operating Area

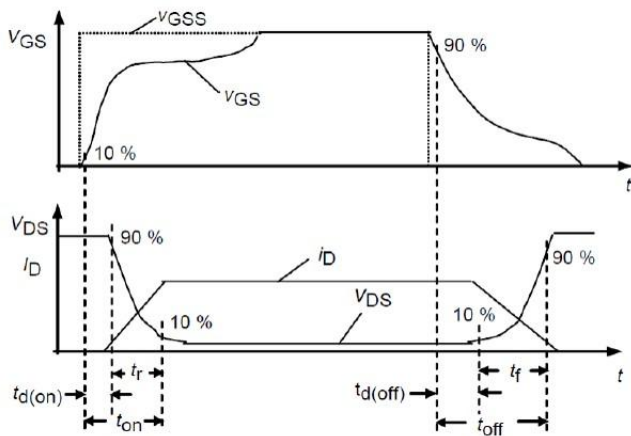


Figure 23. Switching Times Definition

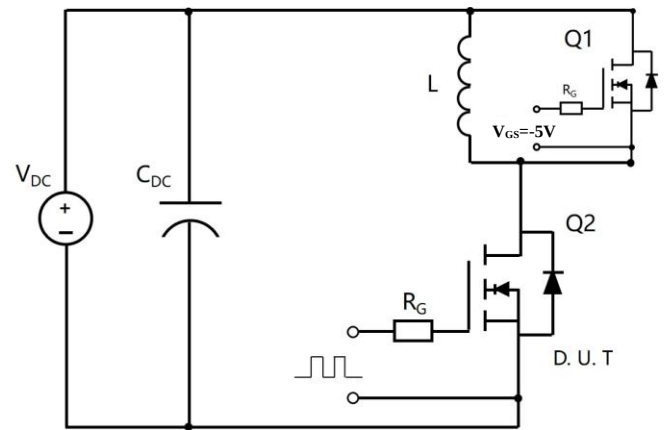
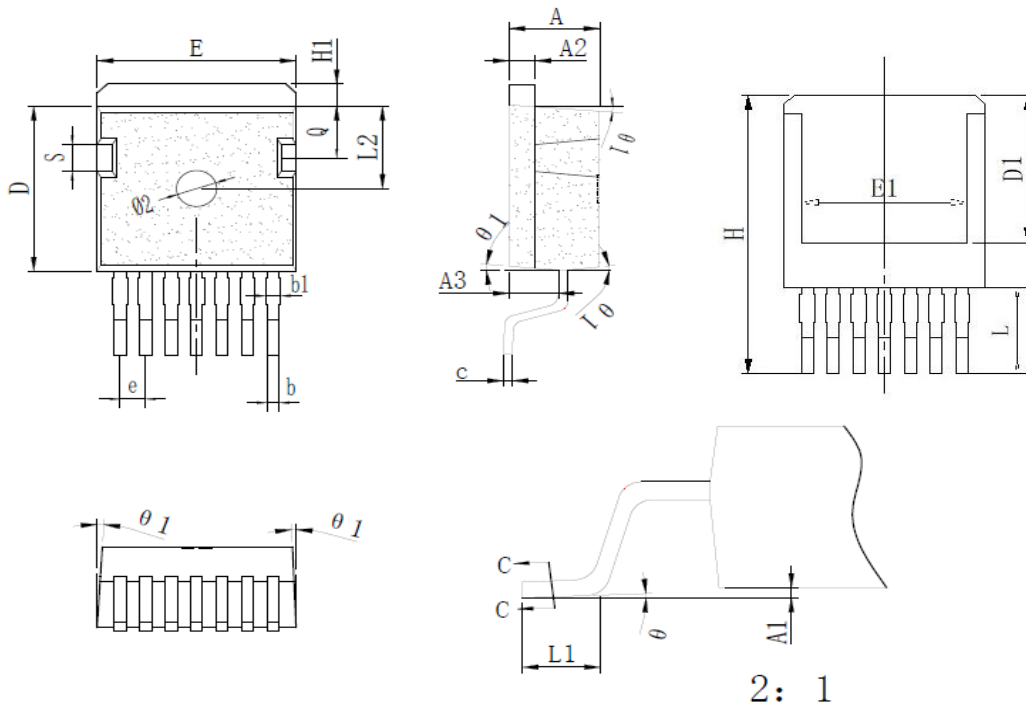


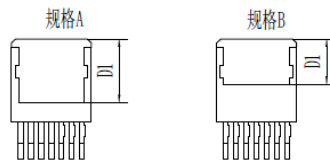
Figure 24. Clamped Inductive Switching Waveform Test Circuit



■Outline Dimensions



* 为关键管控尺寸



SYMBOL	mm		
	MIN	NOM	MAX
*A	4.30	4.40	4.50
*A1	0.00	0.10	0.20
*A2	1.22	1.27	1.32
*A3	2.30	2.40	2.50
*b	0.50	0.60	0.70
*b1	-	-	0.63
*c	0.45	0.50	0.55
*D	9.15	9.30	9.45
D1	规格A: 8.00REF		
	规格B: 5.70REF		
*E	10.12	10.16	10.20
E1	8.20	8.40	8.60
*e	1.25	1.27	1.29
*H	14.85	15.00	15.15
H1	1.10	1.20	1.30
*L	4.50	4.70	4.90
L1	1.70	2.00	2.30
L2	4.55	4.65	4.75
S	1.40	1.50	1.60
Q	2.80	2.90	3.00
* θ	0°	2.5°	8°
$\theta 1$	5°	7°	9°



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