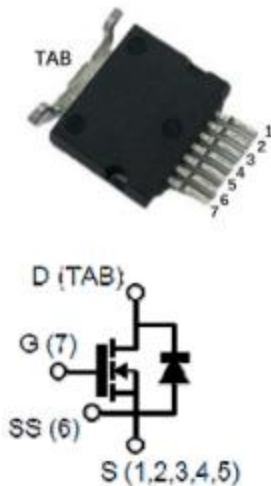


Silicon Carbide Power MOSFET (N-Channel Enhancement)

V_{DS}	750V
I_D (25°C)	62A
$R_{DS(on)}$	25mΩ



Features

- High speed switching
- Essentially no switching losses
- Reduction of heat sink requirements
- Maximum working temperature at 175 °C
- High blocking voltage
- Fast Intrinsic diode with low recovery current
- High-frequency operation
- Halogen free, RoHS compliant

Typical Applications

Typical applications are in power factor correction(PFC), solar inverter, uninterruptible power supply, motor drives, photovoltaic inverter, electric car and charger.

Mechanical Data

- **Package:** T2PAK
- **Terminals:** Tin plated leads
- **Polarity:** As marked

■Maximum Ratings ($T_c=25^\circ\text{C}$ Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	VALUE	TEST CONDITIONS	NOTE
Device marking code			D207535T2YG3		
Drain source voltage @ $T_j=25^\circ\text{C}$	$V_{DS,max}$	V	750	$V_{GS}=0V, I_D=100\mu A$	
Gate source voltage @ $T_j=25^\circ\text{C}$	$V_{GS,max}$	V	-8/+22	Absolute maximum values (AC f > 1Hz, duty cycle < 1%)	
Gate source voltage @ $T_j=25^\circ\text{C}$	$V_{GS,op}$	V	-5/+18	Recommended operational values	
Continuous drain current @ $T_c=25^\circ\text{C}$	I_D	A	62	$V_{GS}=18V, T_c=25^\circ\text{C}$	Fig.17
Continuous drain current @ $T_c=100^\circ\text{C}$			42	$V_{GS}=18V, T_c=100^\circ\text{C}$	
Pulsed drain current	$I_{D(pulsed)}$	A	230	Pulse width t_p limited by $T_{j,max}$	Fig.22
Avalanche energy, Single Pulse	E_{AS}	mJ	964	$V_{DD}=75V, L=10mH$	
Power Dissipation	P_{TOT}	W	205	$T_c=25^\circ\text{C}, T_j = 175^\circ\text{C}$	Fig.16
Power Dissipation			102.7	$T_c=100^\circ\text{C}, T_j = 175^\circ\text{C}$	
Operating junction and Storage temperature range	T_j, T_{stg}	$^\circ\text{C}$	-55 to +175		
Soldering temperature	T_L	$^\circ\text{C}$	260	1.6mm (0.063") from case for 10s	
Mounting torque	T_M	Nm	0.6	M3 screw Maximum of mounting process: 3	



■Static Electrical Characteristics (Tc=25°C unless otherwise specified)

PARAMTETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Gate threshold voltage	$V_{GS(th)}$	V	2.0	3.0	4.0	$V_{DS}=V_{GS}$, $I_D=12mA$	Fig.4, 11
				2.2		$V_{DS}=V_{GS}$, $I_D=12mA$, $T_j=175^\circ C$	
Drain source breakdown voltage	$V_{(BR)DSS}$	V	750			$V_{GS}=0V$, $I_D=100\mu A$	
Drain source leakage current	I_{DSS}	μA		10	100	$V_{DS}=750V$, $V_{GS}=0V$	
Gate source leakage current	I_{GSS}	nA		10	100	$V_{GS}=18V$, $V_{DS}=0V$	
Current drain source on-state resistance	$R_{DS\ ON}$	m Ω		35		$V_{GS}=15V$, $I_D=30A$	Fig.5, 6, 7
				25	33	$V_{GS}=18V$, $I_D=30A$	
				42		$V_{GS}=15V$, $I_D=30A$, $T_j=175^\circ C$	
				33		$V_{GS}=18V$, $I_D=30A$, $T_j=175^\circ C$	
Transconductance	g_f	S		22		$V_{DS}=20V$, $I_D=30A$	Fig.4
				21		$V_{DS}=20V$, $I_D=30A$, $T_j=175^\circ C$	

■Dynamic Electrical Characteristics (Tc=25°C unless otherwise specified)

PARAMTETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Input capacitance	C_{iss}	pF		3465		$V_{DS}=500V$, $V_{GS}=0V$, $T_j=25^\circ C$, $f=1MHz$, $V_{AC}=25mV$	Fig.13, 14
Output capacitance	C_{oss}			250			
Reverse capacitance	C_{rss}			6			
Coss stored energy	E_{oss}	μJ		60.5			Fig.15
Gate source charge	Q_{gs}	nC		37		$V_{DS}=500V$, $V_{GS}=-5/+18V$, $I_D=30A$	Fig.12
Gate drain charge	Q_{gd}			70			
Gate charge	Q_g			132			
Internal gate resistance	R_g	Ω		1.6		$f=1MHz$	

■Switching Characteristics (Tc=25°C unless otherwise specified)

PARAMTETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Turn on delay time	$t_{d(on)}$	ns		14		$V_{DD}=500V$, $V_{GS}=-5/+18V$, $I_D=30A$, $R_g=2.4\Omega$, $L=100\mu H$	Fig.21
Rise time	t_r			18			
Turn off delay time	$t_{d(off)}$	ns		24			
Fall time	t_f			13			



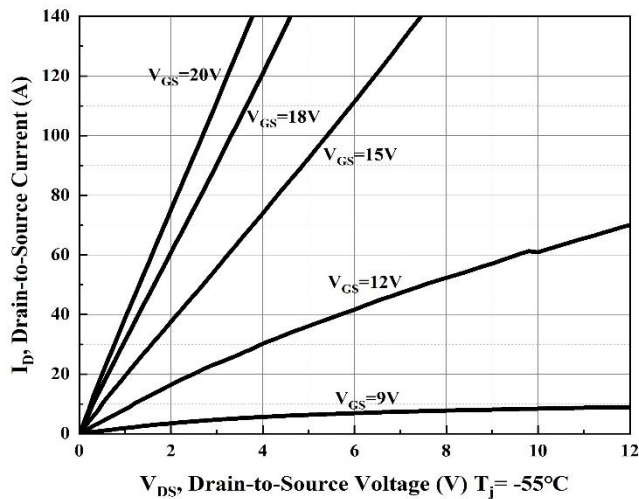
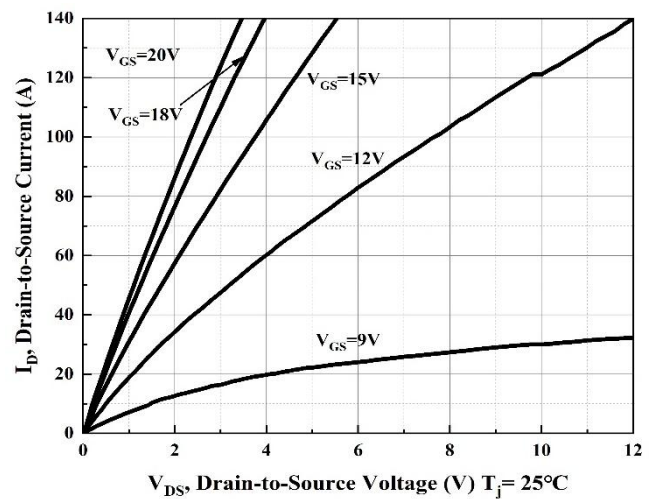
Turn on switching energy	E_{on}	uJ		116.6		$V_{DD}=500V, V_{GS}=-5/+18V, I_D=30A,$ $R_g=2.4\Omega, L=100\mu H$	Fig.19, 20
Turn off switching energy	E_{off}			71.2			

■Body diode characteristics ($T_c=25^\circ C$ unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Diode forward voltage	V_{SD}	V		4.6		$V_{GS}=-5V, I_{SD}=15A$	Fig.8
				2.8		$V_{GS}=0V, I_{SD}=15A, T_j=175^\circ C$	Fig.9
Continuous diode forward current	I_S	A		37		$V_{GS}=-5V, T_c=25^\circ C$	
Reverse recovery time	t_{rr}	nS		19		$V_R=500V, V_{GS}=-5V, I_D=30A,$ $di/dt=1000A/\mu S$	
Reverse recovery charge	Q_{rr}	nC		163			
Peak reverse recovery current	I_{rrm}	A		10			

■Thermal Characteristics ($T_a=25^\circ C$ Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Typ.
Thermal resistance	$R_{\theta J-C}$	$^\circ C/W$	0.73

■Typical CharacteristicsFigure 1. Output Characteristics $T_j = -55^\circ C$ Figure2. Output Characteristics $T_j = 25^\circ C$

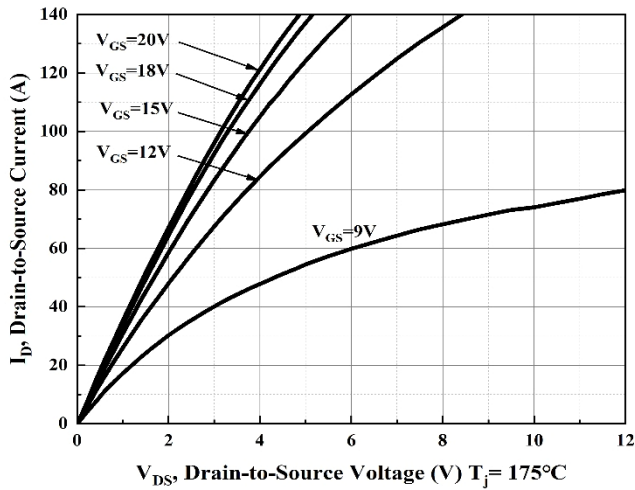


Figure 3. Output Characteristics $T_j = 175^\circ\text{C}$

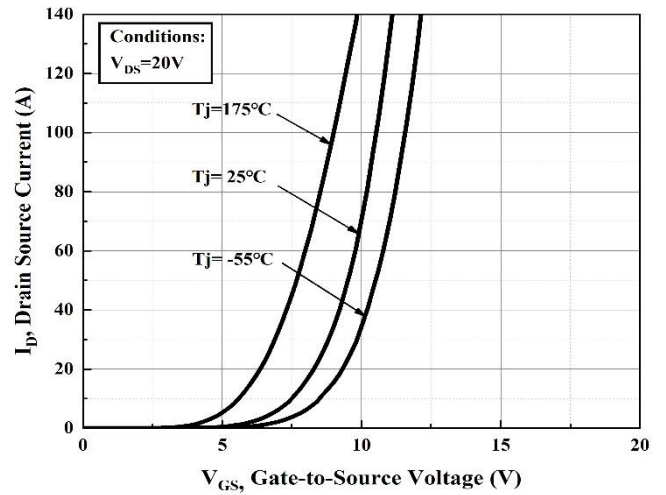


Figure 4. Transfer Characteristics for Various Junction Temperature

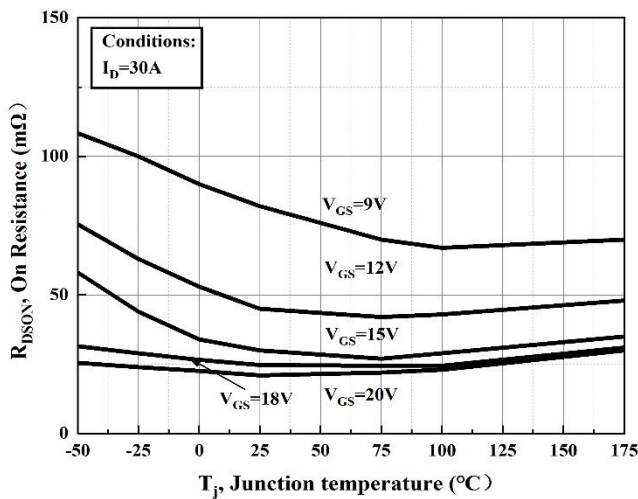


Figure 5. On-resistance vs. Temperature for Various Gate Voltage

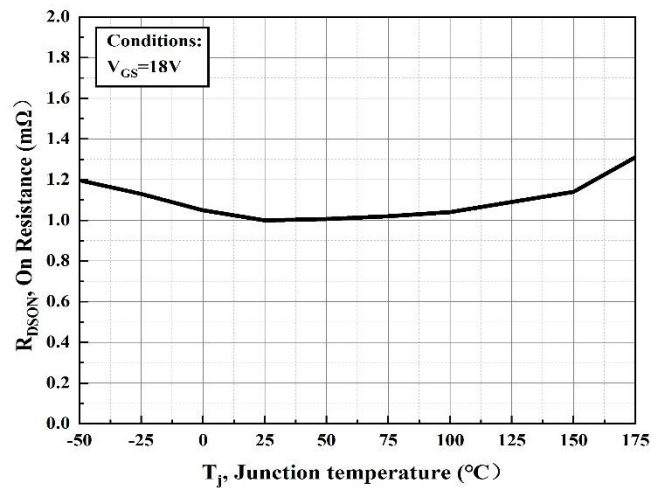


Figure 6. Normalized on-resistance vs. Temperature

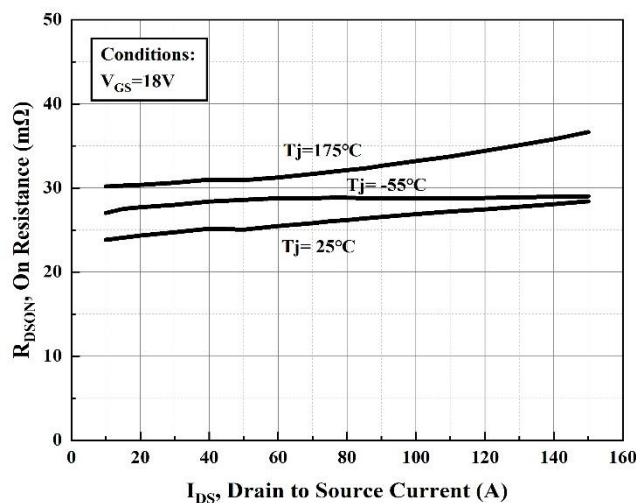


Figure 7. On-resistance vs. Drain Current

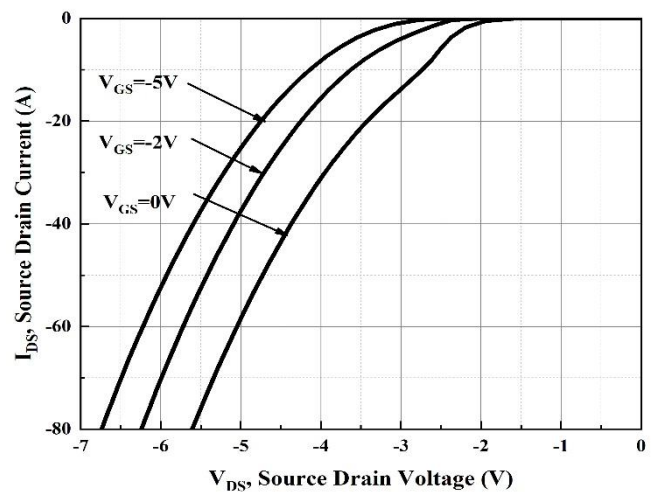


Figure 8. Body Diode Characteristic at $T_j = 25^\circ\text{C}$

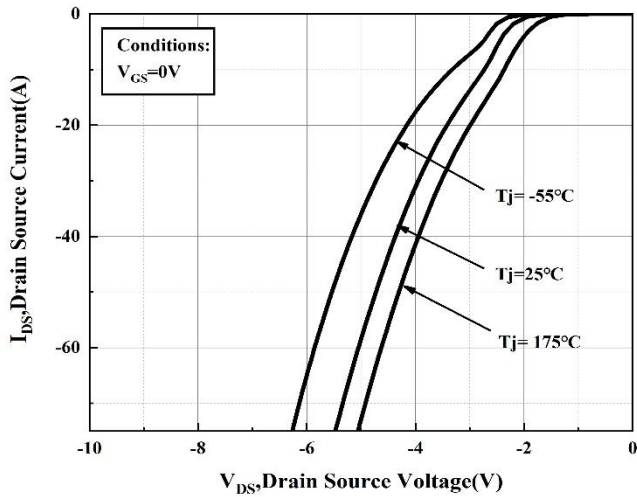


Figure 9. Body Diode Characteristic

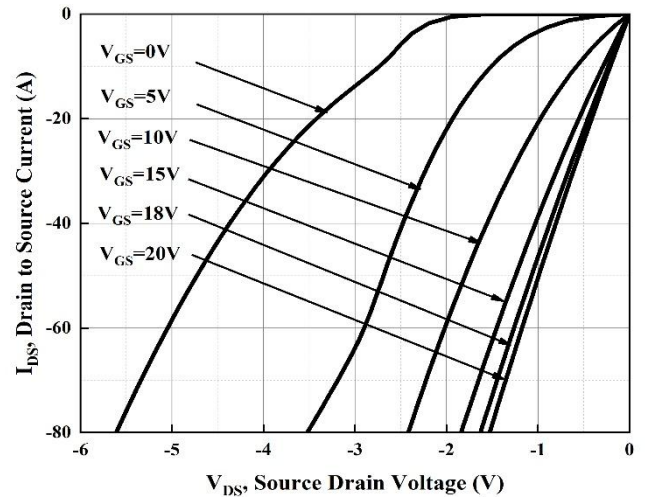


Figure 10. 3rd quadrant Characteristic at Tj= 25 °C

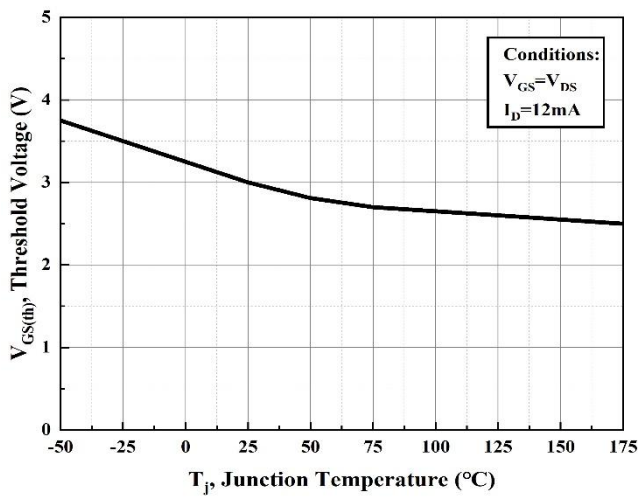


Figure 11. Threshold Voltage vs. Temperature

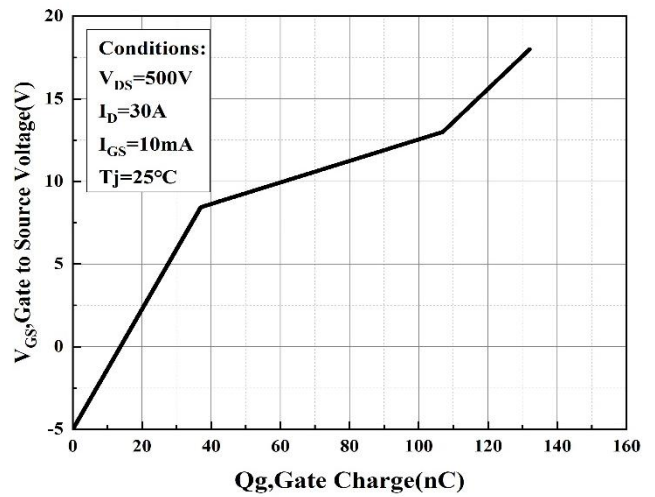


Figure 12. Gate Charge Characteristic

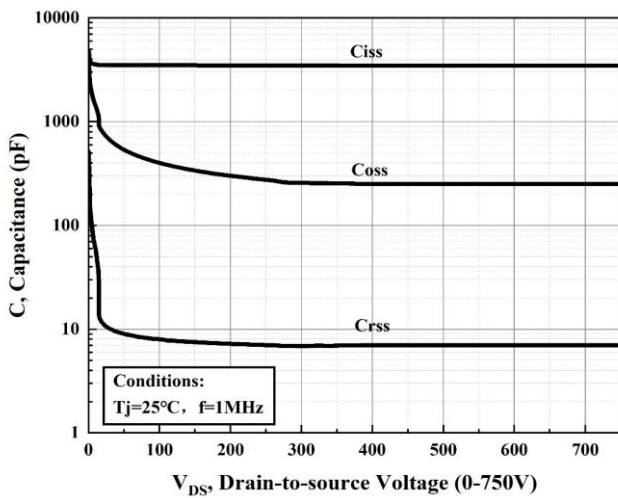


Figure 13. Capacitances vs. Drain Source Voltage (0-750V)

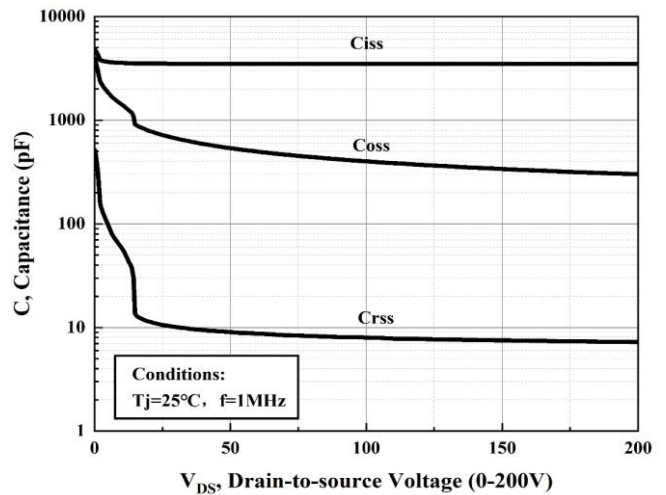


Figure 14. Capacitances vs. Drain Source Voltage (0-200V)

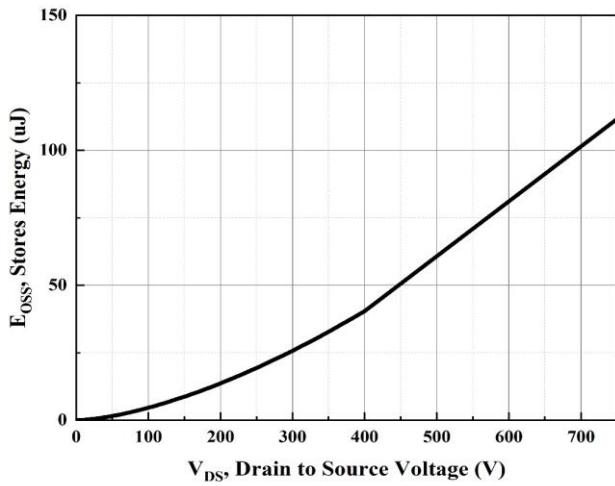


Figure 15. Output Capacitor Stored Energy

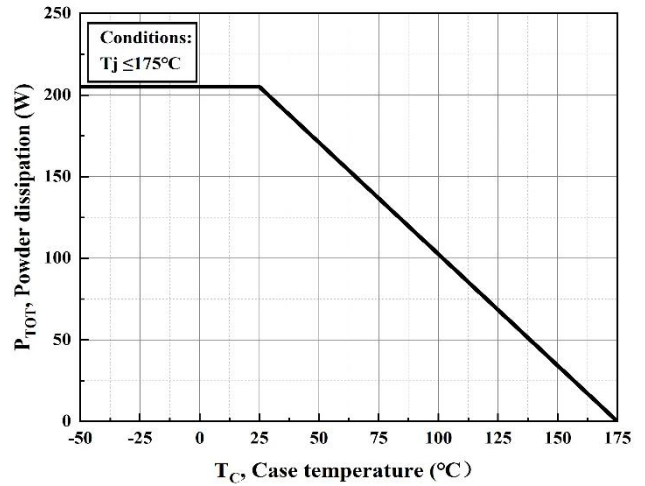


Figure 16. Maximum Power Dissipation Derating vs. Case Temperature

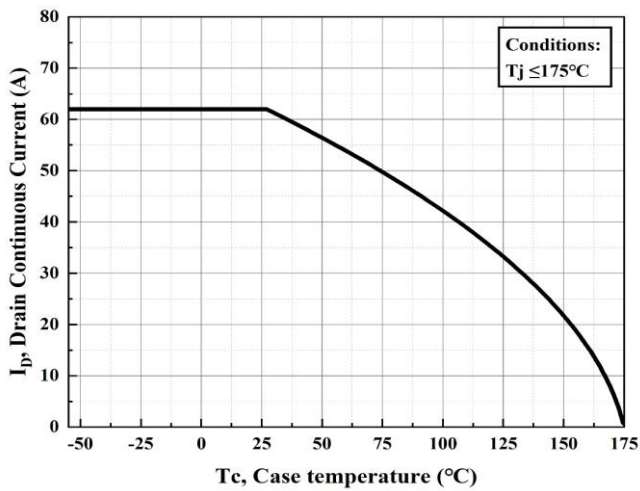


Figure 17. Continuous Drain Current Derating vs. Case Temperature

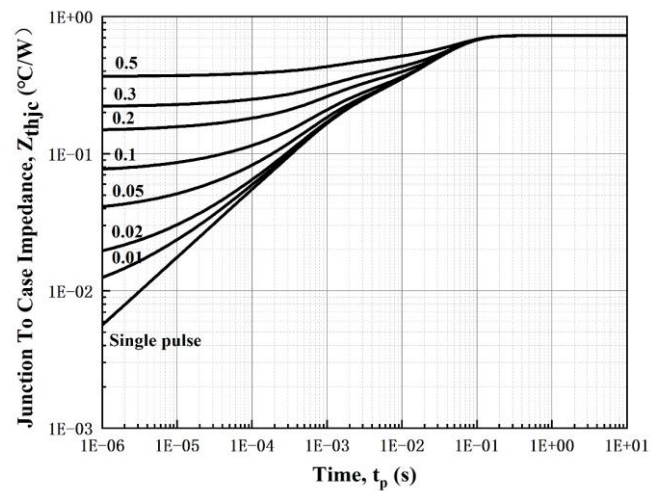


Figure18 Transient Thermal Impedance (Junction - Case)

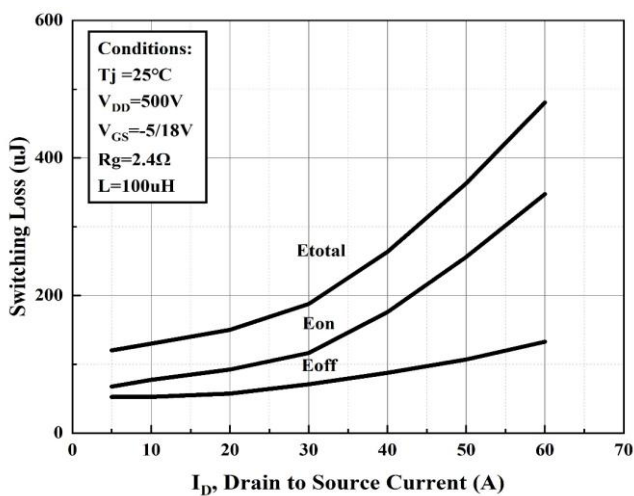


Figure 19. Clamped Inductive Switching Energy vs. Drain Current

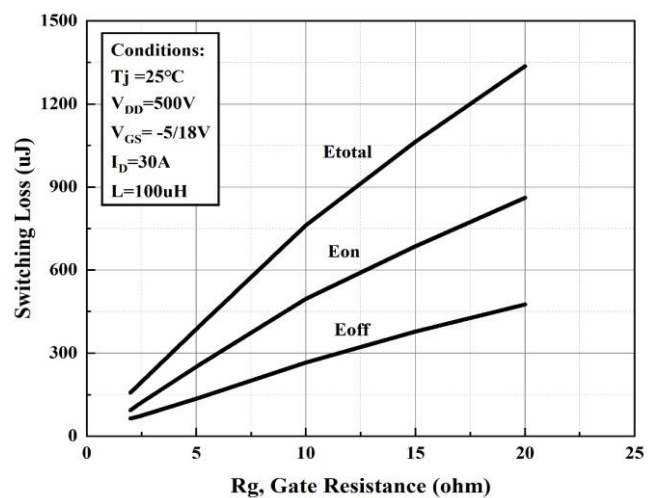


Figure 20. Clamped Inductive Switching Energy vs. Rg

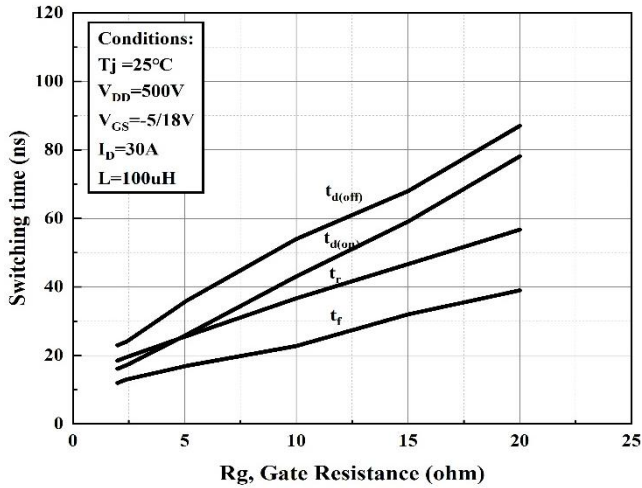


Figure 21. Switching Times vs. R_g

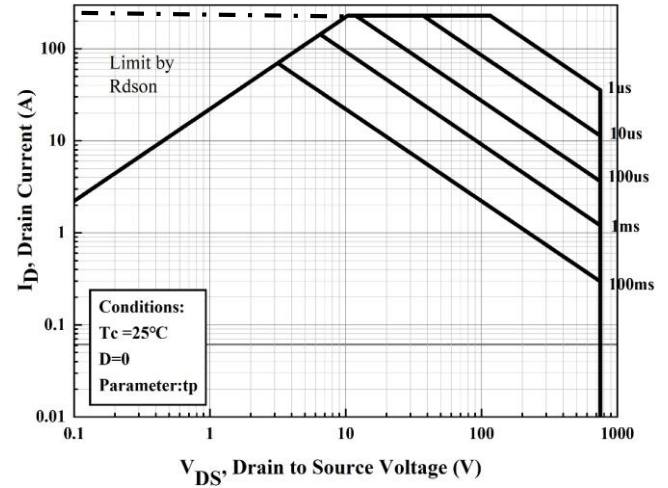


Figure 22. Safe Operating Area

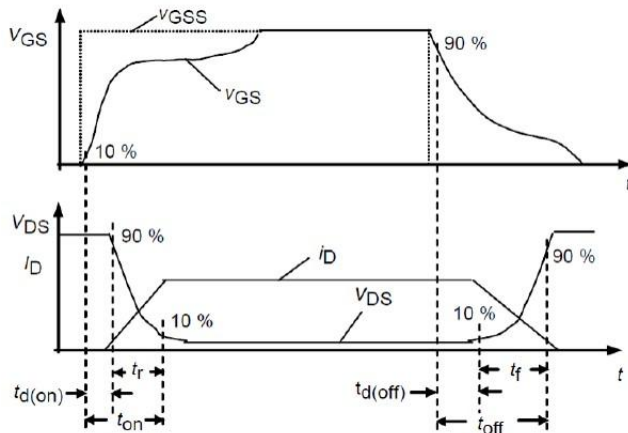


Figure 23. Switching Times Definition

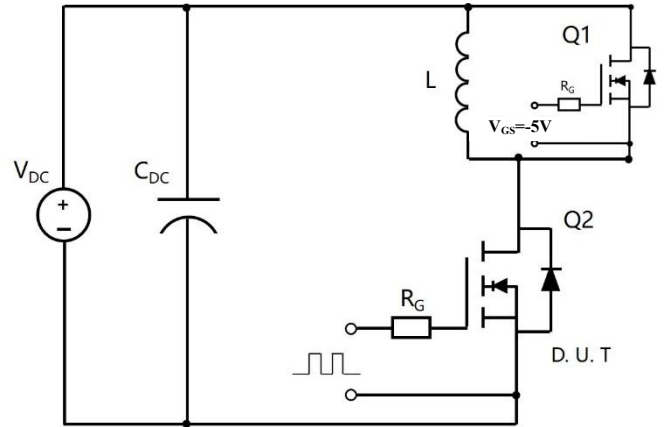
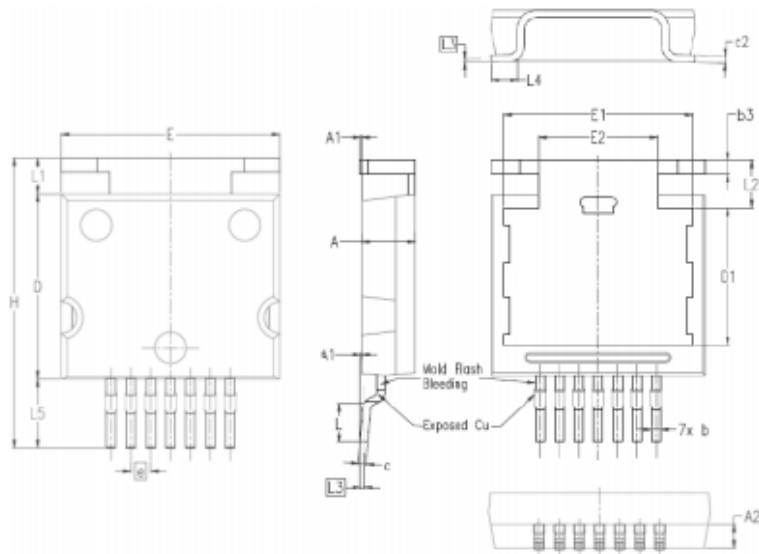


Figure 24. Clamped Inductive Switching Waveform Test Circuit

■Outline Dimensions



Symbol	mm		
	Min	Typ	Max
A	3.30	3.50	3.70
A1	/	0.10	0.25
A2	1.30	1.50	1.70
b	0.50	0.60	0.70
b3	0.80	0.90	1.00
c	0.40	0.50	0.60
c2	0.40	0.50	0.60
D	11.70	11.80	11.90
D1	8.80	9.00	9.20
E	13.60	14.00	14.40
E1	12.00	12.40	12.80
E2	7.60	7.80	8.00
e	1.27 BSC		
H	17.70	18.50	19.30
L	1.90	2.50	3.10
L1	2.30 REF		
L2	2.85	3.10	3.35
L3	0.25 BSC		
L4	1.25	1.85	2.45
L5	4.40 REF		



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